

Attorney Docket No. NVID001/00US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of Priem *et al.*

Serial No.: 09/056,656

Filed: April 7, 1998



Examiner: U. Chauhan

Art Unit: 2671

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Technology Center 2600

For: **Texture Cache for a Computer Graphics Accelerator**

Commissioner for Patents
Washington, D.C. 20231

TRANSMITTAL OF AMENDMENT/RESPONSE

Enclosed are the following documents in response to the Final Office Action mailed December 12, 2000 for the above-identified application:

- ☒ [X] Amendment/Response After Final
- ☒ [X] Appendix: Amended Claims in Marked-Up Form
- ☐ [] Petition for Extension of Time
- ☐ [] Information Disclosure Statement
- ☐ [] Notice of Appeal
- ☐ [] Associate Power to Prosecute
- ☐ [] Revocation and New Power to Prosecute
- ☐ [] Change of Address
- ☐ [] Other:

WJ

The fee has been calculated as follows:

FOR:	Claims After - Amend.	Claims Prev. = Paid	Extra Claims ¹	Small Rate	Entity Fee	Other Than a Small Entity Rate	Fee	Total Claim Fee
Total Claims	28	41	0	\$9		\$18		\$0.00
Independent Claims	4	4	0	\$40		\$80		\$0.00
Multiple Dependent Claims Not Previously Presented				\$135		\$270		\$0.00
TOTAL								\$0.00

- [] Check No. _____ in the amount of \$_____ for the total fee is attached.
- [] Please charge \$_____ to Deposit Account No. 50-1283 for the total fee.
This paper is being submitted in duplicate.

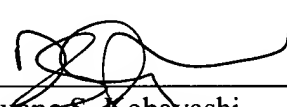
The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§1.16, 1.17, and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 50-1283.

Dated: 3/12/01

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Respectfully submitted,
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Amend C
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PATENT 3-1501
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For: **Texture Cache For A Computer Graphics Accelerator**

Assistant Commissioner for Patents
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AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. §1.116

Responsive to the Final Office Action of December 12, 2000, Applicants respectfully request reconsideration of the above-identified application in view of the following amendments and remarks.

OK to enter
OK

IN THE CLAIMS

Please amend claims 42, 52, 62 and 66 as follows:

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42. (Amended) A computer system, comprising:

a bus;

a central processing unit coupled to said bus; and

a graphics accelerator coupled to said bus, said graphics accelerator including a texture cache system, said texture cache system including a texture cache memory that stores texels to be used by a texel value generating circuit, a replacement control